Fermi National Accelerator Laboratory

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Engineering Note

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Project: Run II A
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Subject: Downloading the latest Platform firmware into Sequencer and Sequencer Controller

This is an attempt to have a common, known place to obtain the latest firmware files that are used in the Platform Sequencers and Sequencer Controllers for both the SMT and CFT systems.   This can be useful when verifying the integrity of firmware or when converting one spare type to another.
The files are located at: **D0server4\Projects\Electronics\PlatformFirmware**
There are separate CFT and SMT directories, and each of these directories has a separate Sequencer and Seq Controller directory.

There are separate .jcf files for each card. These jcf files can be modified by the user to correctly reflect the path to the server on his computer. Note that, for the Sequencer, only the .pof files typically changed are included in the jcf files. The EPLDs on the JTAG chain common to both systems are Appender and Pcutil. 1553 and Laser interface are PLDs which are not on the JTAG chain.

I hope to be able to keep this folder up to date with the help of the key systems people who may make changes to any of the files.